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F-6810 Form PTO-1449 U.S. Department of Commerce Atty. Docket No.: F-6810 Serial No.: 09/772,027 Patent and Trademark Office (Rev. 7-80) Applicant: Kenji SHIGEKI, et al. 42-44F (F-49) INFORMATION DISCLOSURE CITATION Filing Date: January 29, 2001 (Use several sheets if necessary) Group: **U.S. PATENT DOCUMENTS** Date Examiner **Document Number** Name Class **Subclass** Filing Date Initial Appropriate FOREIGN PATENT DOCUMENTS Date Country Class **Document Number Subclass Translation** Yes No 91 11765 8/8/1991 WO N MM 0 926 589 6/30/1999 EP N rnw TRANSLATION KEY: * English Abstract. F Concise statement of relevance provided in foreign search report. ^c Concise statement of relevance provided in specification. ^s Concise statement of relevance provided in IDS. P Relevant portion of reference translated. ^o English abstract only - copy of reference in pct search. OTHER INFORMATION DISCLOSURE CITATIONS (Including Author, Title, Date, Pertinent Pages, Etc.) 9/28/1993 A High-Speed RISC CPU Using the QL16x24 FPGA Bruce Kleinman et al. MMI WESCON/93. Conference Record, San Francisco, CA, USA pages 245-250 **EXAMINER** DATE CONSIDERED W, TREAT 11/23/2004

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MMT	5/10/1988 Optimized for Performance and Cost/Performance: The AM29000 Family Chu, Paul Electro, Electronic Conventions Management, Los Angeles, US, XP000118801 Vol. 13 pages 42-4, 1-7										
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